



**TIME TABLE FOR BACHELORS DEGREE PROGRAMMES
SPRING SEMESTER 2011**

DEPARTMENT OF ELECTRONIC ENGINEERING

TIME TABLE FOR: FINAL YEAR BATCH: 2008-2009

REVISION-I

EFFECTIVE DATE: 09-01-2012


F/QSP 11/04/04

SECTION – A					Room No: 17	
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
08:30 – 09:20		VLSI		DSP		{SSD(3)/ VLSI A1} LABS
09:20 – 10:10	SSD	VLSI	VLSI	DSP		
10:10 – 11:00	{DSP A1 /CS-II (3)} LABS		DSP	SSD		{SSD(1)/ VLSI A2} LABS
11:30 – 12:20	SSD	CS-II(2) LAB	CS-II			SSD(2) LAB
12:20 – 13:10	CS-II		CS-II			
13:10 – 14:00	Lunch / Prayer Time					
14:00 – 14:50	{DSP A2 /CS-II(1)} LABS		CS-II(4)			SSD(4) LAB
14:50 – 15:40						

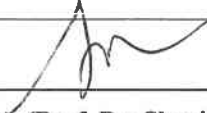
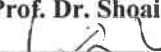
SECTION – B					Room No: 16	
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
08:30 – 09:20		DSP	VLSI			
09:20 – 10:10	DSP	DSP		SSD(7) LAB	SSD(6) LAB	
10:10 – 11:00	CS-II	CS-II(7) /VLSI B1 LAB	CS-II	{VLSI B2 / CS-II(6) /SSD(5)}LAB	{SSD(8)/ CS- II(5)}LABS	
11:30 – 12:20	CS-II	VLSI	DSP LAB B1	SSD		
12:20 – 13:10	SSD	VLSI		SSD		
13:10 – 14:00	Lunch / Prayer Time					
14:00 – 14:50			DSP LAB B2	CS-II(8) LAB		
14:50 – 15:40						

FOR SSD/CS-II LABS THERE ARE 8 GROUPS IN TOTAL {04 GROUPS(01 TO 04) OF SECTION A AND 04 GROUPS(05 TO 08) OF SECTION B}

Course No.	Course Title	Abbreviation	Course/Practical Teacher Name
EL-407	VLSI System Design	VLSI	Mr. Hashim Raza /Mr. Abdul Raheem
EE-493	Digital Signal Processing	DSP	Ms. Andaleeb Gufran
TC-492	Communication System II	CS-2	Mr. Adeel Razi/ Ms. Uzma Butt
EL-433	Solid State Devices	SSD	Ms. Sadia Faraz/ Ms. Hira Imtiaz
EL-401	Electronics Engineering Project		

Reviewed By: 
Class Adviser (Ms. Hira Imtiaz)
Date: 06/01/2012

checked

Approved BY: 
Chairman (Prof. Dr. Shoaib Zaidi)
Date: 

CLD

Shiddiq
(...)