



DIRECTORATE OF INDUSTRIAL LIAISON

NED University of Engineering & Technology

University Road, Karachi-75270 Pakistan

Tel: (+92-21) 99261261-8, Ext: 2274, 2218, Email: dil@neduet.edu.pk



'SAY NO TO CORRUPTION'

03-09-2025

Hardware Design Engineer (FPGA)

Mandatory Requirements

- BS/BE or Master's in Electrical/Computer Engineering with CGPA of 3 or above
- Experience in FPGA and embedded systems designs
- Proficient in Verilog /VHDL and FPGA design tools (e.g Vivado, Quartus)
- Familiar with Raspberry Pi/ STM32 microcontrollers and embedded C/C++/Python
- Strong grasp of digital signal processing (DSP) and troubleshooting skills
- Excellent communication and problem-solving abilities

Preferred Requirements

- Experience with high-speed interfaces & communication protocols
- Knowledge of C++/ Python classes and Object Oriented Programming (OOP) for DSP applications
- Familiarity with FPGA boards and Software Defined Radios (SDR)

Why INTELL

- Chance to collaborate with multidisciplinary team
- Great learning environment
- Market competitive remuneration based on qualification & skillset

JOIN US

Send your resume at: intell.ndt@gmail.com

With subject title "Hardware Design Engineer - FPGA"

Please mention your qualification, experience & CGPA in resume

Job Location: NUST PNEC Karachi

Integrated
Navigational
Terrestrial
Electromagnetic
Lab



Job Responsibilities

- Design & implement FPGA-based hardware solutions using industry-standard tools and methodologies
- Develop and debug HDL code for FPGA systems
- Integrate hardware components into embedded systems
- Optimize hardware designs for performance and efficiency
- Test and validate hardware designs comprehensively
- Document designs and procedures for traceability

Apply latest by 20 Sep 25

NATIONAL UNIVERSITY OF SCIENCE AND TECHNOLOGY (NUST PNEC)

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RF Design Engineer

Integrated
Navigational
Terrestrial
Electromagnetic
Lab



Mandatory Requirements

- BE/ BS - Electrical/ Electronics/ Telecommunications or any relevant field with minimum 3 CGPA (Mention CGPA in your Resume)
- Solid understanding of Electrical Network Theory, Electromagnetic Field Theory, Fourier Analysis, Complex Analysis, Vector Calculus & PDEs

Preferred Requirements

- Final Year Project in RF Modeling or MS Electrical with specialization in RF Modeling
- Expertise in software packages: CST, ANSYS HFSS & SolidWorks
- Inclination towards engineering optimization

Job Responsibilities

- Design & Modeling of Antenna Arrays
- Design of Passive 3D RF Filters
- Multiphysics Analysis of Microwave Devices

Why Intell

- Chance to work on complex R&D projects with multidisciplinary team
- Market competitive remuneration based on qualification & skillset
- Great learning environment & international publication opportunities

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