



PROCUREMENT CELL NED UNIVERSITY OF ENGINEERING & TECHNOLOGY, KARACHI.

BY REGISTERED A.D./COURIER SERVICE.

CONTRACT SCHEDULE

1. Contract No: PC/NED/TC/Misc. Item/8069/2022/

2. Name & Address of Contractor: M/s. Al Majid International

3. Contractor's Tender No. & Date: Nil

07-02-2022

Indenter's Name & Address: Chairman, Telecommunication Engineering Department

5. Indenter's Indent No & Date: 15435

09-12-2021

6. Particulars of stores:

As below.

Item No.	Description of Store	Acctg Unit	Price per A/U	Total Qty Reqd.	Total Value
01 to 04	Specification attached of Item No. 01 to 04	Nos.			489,060/-
	Total Amount with all Taxes				489,060/-

INSTRUCTIONS:

- Store must be delivered to Central Store in due period which will be issued by the Central Store to the Indenter.
- A copy of Delivery Challan must be delivered to Purchase Officer on same day after delivery of Store.
- Bill should be submitted Assistant Director Procurement-II along with GST Invoice.
- Inspection Certificate may be issued by Central Store within Prescribed period as mentioned in clause 15 of the Indent Form.

GRAND TOTAL (Value in Words) Four Hundred Eighty Nine Thousand Sixty Only

7. Date of Delivery: Within 40 Days

If goods not delivered within stipulated time; please immediately intimate to the Procurement Cell.

- 8. Place of Delivery: NED University Campus/Consignee.
- 9. Name & Address of Consignee: Muhammad Adil, Lab Technician







- 10. Performance Security equivalent to 5% of Contract Schedule is required at the earliest.
- 11. Contract Agreement on Stamp papers value equivalent to rate as specified in Stamp Act.
- 12. Dispatch Instruction.
- 13. Inspecting Officer.
- 14. Place at which to be inspected.
- 15. Payment.

Stores should be delivered at firm's premises/free to the consignee at NED University of Engineering and Technology.

The consignee/ Indenting officer or his authorized representative.

At firms premises/ Consignee's end.

- At IIIII3 premises/ consignees end.
- 100% payments to be made on the proof of inspection and Consignee's receipt certificate during year 2021-2022.
- ii.

 ____% Payment in advance along with contract/and balance after complete delivery and in proof of consignee's receipt during the year 2021 2022
- If the supply is not according to the specifications or unsatisfactory, the contract will be rejected and cancelled at the risk and cost of firm.
- iv. If the firm fails to execute the contract/supply order as per condition action will be taken against them which may be their black listing and E.M.S.D. will be forfeited.
- In Case of late delivery L.D. charges @ 2% per month will be imposed. Part payment against part supply is/ not allowed.
- vi. Valid Professional Tax Certificate is required for payment.

Assistant Director Procurement-II
For & on behalf of Vice Chancellor
NED University of Engg. Tech.

The cost is debitable to Head of Account as mentioned in the Sanction order (office order). This issues with the sanction and approval of the Competent Authority of this University.

NOTE

- The firms may send the Inspection call with-in delivery period under intimation to the Assistant Director Procurement in writing.
- The Bill in duplicate along with Inspection certificates on form F/QSP 09/10/00 may be sent to the P.O for payment.

Copy to

- 1. Resident Auditor
- 2. Indenting Officer
- 3. Case File
- 4. Master File
- 5. Manager Central Store



Quotation

Date: 07/02/2022

Page 01 of 04

Kind Attention,
Director Procurement,
M/S NED University of Engineering & Technology,
University Road,
Karachi.

Sub: Offer against TENDER NO. PC/NED/TC/Misc Items/8069/2022

"Procurement of FPGA Board, FPGA Starter Kit, RFS Doughter Card and FPGA DE-1 SOC for Department of Telecommunication Engineering."

Dear Sir,

Below you will find our best possible offer for the subjected tender:

S.No	Description Of Items	No's	Unit Price (PKR)	Total Price(PKR)
01	DE10-Standard The DE10-Standard board has many features that allow users to implement a wide range of designed circuits, from simple circuits to various multimedia projects. The following hardware is provided on the board: FPGA Device • Cyclone V SX SoC—5CSXFC6D6F31C6N • 110K LEs, 41509 ALMs • 5,761 Kbits embedded memory • 6 FPGA PLLs and 3 HPS PLLs • 2 Hard Memory Controllers ARM-Based Hard Processor System (HPS) • 925 MHz, Dual-Core ARM Cortex-A9 MPCore Processor • 512 KB of Shared L2 Cache • 64 KB of Scratch RAM	01	107,800/-	107,800/-
	Multiport SDRAM Controller with Support for DDR2, DDR3, LPDDR1, and LPDDR2 8-Channel Direct Memory Access (DMA) Controller Configuration and Debug Serial Configuration Device – EPCS128 on FPGA On-Board USB Blaster II (Normal Type B USB Connector) Memory Device 64MB (32Mx16) SDRAM on FPGA IGB (2x256Mx16) DDR3 SDRAM on HPS MicroSD Card Socket on HPS			8/2/22
	Communication Two USB 2.0 Host Ports (ULPI Interface with USB Type A Connector) on HPS USB to UART (Micro USB Type B Connector) on HPS 10/100/1000 Ethernet on HPS PS/2 Mouse/Keyboard IR Emitter/Receiver Connectors One 40-pin Expansion Header (Voltage Levels: 3.3V) One HSMC Connector(Configurable I/O Standards 1.5/1.8/2.5/3.3V) One 10-Pin ADC Input Header One GPIO Interface 1 on HPS			ERA





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S.No	Description Of Items	No's	Unit Price (PKR)	Total Price(PKR)
	Display	7	()	11100(11111)
	24-bit VGA DAC			
all the terms of	128x64 Dots LCD Module with Backlight on HPS			
	Audio			
8	24-bit CODEC, Line-in, Line-out, and Microphone-In Jacks			
	Video Input		File W	
=	TV Decoder (NTSC/PAL/SECAM) and TV-In Connector		The I was a second	
	ADC		;	
	Sample Rate: 500 KSPS			
	Channel Number: 8, Resolution: 12 bits	, e		
	• Analog Input Range : 0 ~ 4.096 V	- H		
	Switches, Buttons and Indicators	III Teres		
	• 5 User Keys (FPGA x4, HPS x1)			
	• 10 User Switches (FPGA x10)			
	• 11 User LEDs (FPGA x10; HPS x 1)			
	 2 HPS Reset Buttons (HPS_RST_n and HPS_WARM_R\$T_n)7-Segment Display 			
	x6			
	Sensors			Alleganic of the
	G-Sensor on HPS			
	Power			
	• 12V DC Input	- 13		Service Control
02	Cyclone V GX Starter Kit	02	81,000/-	162,000/-
	FPGA Device		01,000	102,000/
	 Cyclone V GX 5CGXFC5C6F27C7N Device 			21
	77K Programmable Logic Elements	19	Boulet Miles	
	4884 Kbits embedded memory	3/ 1/ 1/		Te To
	Six Fractional PLLs		The same of the	
	Two Hard Memory Controllers			I in
	Six 3.125G Transceivers			
+	Configuration and Debug		A NOTE OF BUILDING	
	Quad Serial Configuration device – EPCQ256 on FPGA			
	On-Board USB Blaster (Normal Type-B USB connector)			
	JTAG and AS mode configuration supported			
	Memory Device			
	4Gb LPDDR2 x32 bits data bus			2
-	4Mb SRAM x16 bits data bus		1	(X) of prom
	Communication			
	UART to USB	THE REAL PROPERTY.		8/2/22
100	Expansion I/O			100
	HSMC x 1(voltage levels: 2.5V), including 4-lanes 3.125G transceiver			130
	• 2x20 GPIO Header (voltage levels: 3.3V)	2-1		he was a second
	Arduino header, including analog pins			
	SMA x 4 (DNI), one-lane 3.125G transceiver			/
	Display			/
	HDMI TX, compatible with DVI v1.0 and HDCP v1.4			
	Audio			
	24-bit CODEC, Line-in, line-out, and microphone-in jacks			
100	Switches, Buttons, LED, and 7-Segments			
	• 18 LEDs		15.5	S
	10 Slide Switches 4 Debounced Push Buttons	1	INTE	BAV
	I CPU reset Push Buttons Four 7-Segments		119/	1:211
			12 KND	Adul ZI
			II & I WALL	1171111
			11 = 1	17/211





	Description Of Items	No's	Unit Price (PKR)	Total Price(PKR
	Micro SD Card Socket		(= 111)	11100(1111
	• Provides SPI and 4-bit SD mode for SD Card access			
	ADC		1000	4 16
	 12-Bit Resolution, 500Ksps Sampling Rate. SPI Interface. 			
	8-Channel Analog Input. Input Range: 0V ~ 4.096V. /			
	Power			
	12V DC input	7 11 8 11		
	RFS Radio Frequency and Sensor (RFS) Daughter card	02	26,600/-	52.2004
	Features	02	20,000/-	53,200/-
	• Wi-Fi, using ESP-WROOM-02 module, up to 1,00 meter range			1 5 5
	Bluetooth SPP, using HC-05 module, up to 10 meter range			
	9 axis conserved and the constitution of the conserved and the con			
	9-axis sensor: accelerometer, gyroscope, magnetometer			
	Ambient light sensor			
	Humidity and temperature sensor			
	UART to USB	le man		
X.	2x6 TMD GPiO Header			
	DE1-SoC Board	01	05.005	
	The DE1-SoC board has many features that allow users to implement a wide range of	01	95,000/-	95,000/-
	designed electrics, from simple circuits to various multimedia projects			
	The following hardware is provided on the board:			
	FPGA Device			
	Cyclone V SoC 5CSEMA5F31C6 Device			
	Duai-core ARM Cortex-A9 (HPS)			
	85K Programmable Logic Elements			
	4.450 Kbits embedded memory			
	6 Fractional PLLs			
	• 2 Hard Memory Controllers			
	Configuration and Debug			
	Serial Configuration device – EPCS128 on FPGA		1	
	On-Board USB Blaster II (Normal type B USB connector)			
	Memory Device			
	64MB (32Mx16) SDRAM on FPGA			
	1GB (2x256Mx16) DDR3 SDRAM on HPS			
	Micro SD Card Socket on HPS			
	Communication		\sim	200
	Two USB 2.0 Host Ports (ULPI interface with USB type A connector) on HPS	4 1 10	Ų	Mohum
	UART to USB (USB Mini B connector)			1
i	• 10/100/1000 Ethernet			8/2/22/
		() 6	\ <u>'</u>	1 1000
	PS/2 mouse/keyboard	1 Kg	<i>P</i>	/
ļ	• IR Emitter/Receiver	109019	1	
	Connectors	B KOKN	J	/
	Two 40-pin Expansion Headers (voltage levels: 3.3V)	01		
	One 10-pin ADC Input Header			
	One LTC connector (One Serial Peripheral Interface (SPI) Master ,one I2C and			
1	one GPIO interface)			
	Display			
	24-bit VGA DAC	1	!	
	Audio			
	• 24-bit CODEC, Line-in, line-out, and microphone-in jacks			
	Video Input	1		NTERN
	TV Decoder (NTSC/PAL/SECAM) and TV-in connector		1	0
-	2 Decoder (N13C/1 AL/SECANI) and 1 V-in connector		1/4	4/1

A-213/1, Block 10-A, WCHS Gulshan-e-Iqbal Behind Lal Flats, Karachi. © 021-34177497 0331-2324541 almajidinternationalco@gmail.com



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S.No	Description Of Items	No's	Unit Price (PKR)	Total Price(PKR)
	ADC		(=====)	11100(1101)
	sample rate: 500 KSPS			
i	• Channel number: 8			1
	Resolution: 12 bits			
	 Analog input range: 0 ~ 4.096 V 			
	Switches, Buttons and Indicators	e Av		realization of the same
	• 4 User Keys (FPGA x4)	- 17		
	• 10 User switches (FPGA x10)			
	• 11 User LEDs (FPGA x10; HPS x i)			
	2 HPS Reset Buttons (HPS_RST_n and HPS_WARM_RST_pt)			
	• Six 7-segment displays			
1	Sensors			
	G-Sensor on HPS			
1	Power / /			
	12V DC input			

<u>Terms & Condition:</u>
Delivery Period: After receiving purchase order within 40 days.
Earnest Money Attached: PKR 25,000/-

Make Terasic

Kind Regards,

Al-Majid International Co.



SUBTOTAL (PKR)	418,000/-
GST (17%) (PKR)	71,060/-
TOTAL PRICE with GST (PKR)	489,060/-

Behind Lal Flats, Karachi.

SINDH PUBLIC PROCUREMENT REGULATORY AUTHORITY

CONTRACT EVALUATION FORM

TO BE FILLED IN BY ALL PROCURING AGENCIES FOR PUBLIC CONTRACTS OF WORKS, SERVICES & GOODS

	NED UNIVERSITY		
NAME OF THE ORGANIZATION / DEPTT.			
PROVINCIAL / LOCAL GOVT./ OTHER	PROVINCIAL		
ΓITLE OF CONTRACT	Procurement of FPGA Starter Kit, RFS Daughter Card		
TENDER NUMBER	PC/NED/TC/Misc. Items/8069/2022		
BRIEF DESCRIPTION OF CONTRACT	Procurement of FPGA Starter Kit, RFS Daughter Card etc		
FORUM THAT APPROVED THE SCHEME	Syndicate		
TENDER ESTIMATED VALUE	0.489 MILLION		
ENGINEER'S ESTIMATE (For civil works only)			
ESTIMATED COMPLETION PERIOD (AS PE	R CONTRACT) 40 days		
TENDER OPENED ON (DATE & TIME)	08-02-2022 10:30 AM		
NUMBER OF TENDER DOCUMENTS SOLD (Attach list of buyers)	03		
NUMBER OF BIDS RECEIVED	03		
NUMBER OF BIDDERS PRESENT AT THE T	TIME OF OPENING OF BIDS 03		
BID EVALUATION REPORT (Enclose a copy)	PROVIDED		
NAME AND ADDRESS OF THE SUCCESSFU	JL BIDDER M/s. AL Majid International		
CONTRACT AWARD PRICE	Rs. 489,060/-		
RANKING OF SUCCESSFUL BIDDER IN EV (i.e. 1 st , 2 nd , 3 rd EVALUATION BID).	ALUATION REPORT The Most Advantageous Bid		
METHOD OF PROCUREMENT USED : - (Tic	k one)		
single stage – one envelope	PROCEDURE YES Domestic/ Local		
SINGLE STAGE – TWO ENVELOPE	PROCEDURE		
two stage bidding procedur	Е		
TWO STAGE – TWO ENVELOPE BI	DDING PROCEDURE		
PLEASE SPECIFY IF ANY OTHER	METHOD OF PROCUREMENT WAS ADOPTED		
O I I B F I E I N SI N C RII	PROVINCIAL / LOCAL GOVT./ OTHER PITLE OF CONTRACT PENDER NUMBER BRIEF DESCRIPTION OF CONTRACT PORUM THAT APPROVED THE SCHEME PENDER ESTIMATED VALUE ENGINEER'S ESTIMATE For civil works only) ESTIMATED COMPLETION PERIOD (AS PERIODER OPENED ON (DATE & TIME) JUMBER OF TENDER DOCUMENTS SOLD Attach list of buyers) RUMBER OF BIDS RECEIVED RUMBER OF BIDDERS PRESENT AT THE TENDER OPENED ENCONTRACT AWARD PRICE EXANKING OF SUCCESSFUL BIDDER IN EVALUATION BID). METHOD OF PROCUREMENT USED: - (Ticket) SINGLE STAGE - ONE ENVELOPE ONE TWO STAGE BIDDING PROCEDUR TWO STAGE BIDDING PROCEDUR TWO STAGE - TWO ENVELOPE BIDDING PROCEDUR TWO STAGE - TWO ENVELOPE BIDDING PROCEDUR		

19)	APPR	OVING AUTHORI	TY FOR AWA	ARD OF CONTRA	ACT _					_
20)	WHET	THER THE PROCU	REMENT WA	AS INCLUDED II	N ANN	NUAL PR	OCUREN Yes	MENT	PLAN	1?
21)	ADVE	RTISEMENT:								
,	.10 (2	action and a second a second and a second an			Yes	T00553-2	1-0109			
	i)	SPPRA Website	1 CDDD 1 1							
		(If yes, give date	and SPPRA Ic	dentification No.)	No				110000000000000000000000000000000000000	
		N. D.			1.0					
	ii)	News Papers (If yes, give name	es of newspape	ers and dates)	Yes					
		0			No					
					37.					
22)	NATU	RE OF CONTRAC	T				Domestic/ Local	V	Int.	
22)	MILLI	THER OLIVE IEIGA	PION CRITTE							
23)		THER QUALIFICATION BID			TS?					
	(If yes	, enclose a copy)	4		A		Yes	V	No	
24)		THER BID EVALUA					Vas	V	No	
		NCLUDED IN BID , enclose a copy)	DING / TEN	DER DOCUMEN	TS?		Yes	-	No	Ш
	(11)	, сполосо и соруу								
25)	WHET	THER APPROVAL	OF COMPET	ENT AUTHORIT	YWA	S OBTAI	NED FO	R US	ING A	
	METH	OD OTHER THAN	OPEN COM	PETITIVE BIDD	ING?		Yes		No	~
20	W. C.	ND SECURITY OF	TABLES ES	O. / . / /	DDED	00				
26)	WASI	BID SECURITY OF	TAINED FR	OM ALL THE BI	DDER	.S?	Yes	~	No	
27)		THER THE SUCCES				ATED	Yes	~	No	
			5 515 (m cus	e or companianeres	,					
28)		THER THE SUCCES	SSFUL BIDD	ER WAS TECHN	IICAL	LY	Yes	~	No	
29)	WHET	THER NAMES OF	THE BIDDER	RS AND THEIR	QUOT	ED PRIC	ES WER	E RE	AD O	UT AT
	THET	IME OF OPENING	OF BIDS?				Yes	V	No	

31) ANY COMPLAINTS RECEIVED (If yes, result thereof) 32) ANY DEVIATION FROM SPECIFICATIONS GIVEN IN 7 (If yes, give details)	Yes No	NO
(If yes, result thereof) 32) ANY DEVIATION FROM SPECIFICATIONS GIVEN IN T	No	NO
		NO
	ГНЕ ТІ	
		ENDER NOTICE / DOCUMENTS
	Yes	
	No	NO
33) WAS THE EXTENSION MADE IN RESPONSE TIME? (If yes, give reasons)	Yes	
	No	NO
34) DEVIATION FROM QUALIFICATION CRITERIA (If yes, give detailed reasons.)	Yes	
	No	NO
35) WAS IT ASSURED BY THE PROCURING AGENCY BLACK LISTED?	0.	Yes V No
36) WAS A VISIT MADE BY ANY OFFICER/OFFICIAL OF SUPPLIER'S PREMISES IN CONNECTION WITH THE BE ASCERTAINED REGARDING FINANCING OF VISIT (If yes, enclose a copy)	PROC	UREMENT? IF SO, DETAILS TO
37) WERE PROPER SAFEGUARDS PROVIDED ON MOB THE CONTRACT (BANK GUARANTEE ETC.)?	ILIZA	Yes No
38) SPECIAL CONDITIONS, IF ANY (If yes, give Brief Description)	Yes	
Dar Sument 41	No	NO
Signature & Official Stamp of Authorized Officer Assistant Director Procurement Cell Procu		
OFFICE USE ONLY		

<u>SPPRA, Block. No.8, Sindh Secretariat No.4-A, Court Road, Karachi</u> Tele: 021-9205356; 021-9205369 & Fax: 021-9206291

Print Save Reset

Integrity Pact

DECLARATION OF FEES, COMMISSION AND BROKERAGE ETC. PAYABLE BY THE SUPPLIERS/CONTRACTORS/CONSULTANTS.

Contract Number: PCINEDITC Misc. item 8069 Dated: 04/63/22
Contract Value: Rs. 1189,060 -
Contract Value: Rs. 1189,060 - Contract Title: Procument of FPGA Stanter Kit, RFS Dugther etc.
M OI MAIL
[Name of Supplier/Contracto/Consultant] hereby declares that it has not obtained or induced the procurement of any contract, right, interest, privilege or other obligation or benefit from
Government of Sindh (GoS) or any administrative subdivision or agency thereof or any other
entity owned or controlled by it (GoS) through any corrupt business practice.
Without limiting the generality of the foregoing, [Name of Supplier/Contractor/Consultant]
represents and warrants that it has fully declared the brokerage, commission, fees etc. paid or payable to anyone and not given or agreed to give and shall not give or agree to give to
anyone within or outside Pakistan either directly or indirectly through any natural or juridical
person, including its affiliate, agent, associate, broker, consultant, director, promoter,
shareholder, sponsor or subsidiary, any commission, gratification, bribe, finder's fee or
kickback, whether described as consultation fee or otherwise, with the object of obtaining or
inducing the procurement of a contract, right, interest, privilege or other obligation or benefit,
in whatsoever form, from Procuring Agency (PA), except that which has been expressly
declared pursuant hereto.
[Name of Supplier/Contractor/Consultant] certifies that it has made and will make full
disclosure of all agreements and arrangements with all persons in respect of or related to the
transaction with PA and has not taken any action or will not take any action to circumvent the
above declaration, representation or warranty.
Ml. Al Mail-
[Name of Supplier/Contractor/Consultant] accepts full responsibility and strict liability for
making any false declaration, not making full disclosure, misrepresenting facts or taking any
action likely to defeat the purpose of this declaration, representation and warranty. It agrees
that any contract, right, interest, privilege or other obligation or benefit obtained or procured
as aforesaid shall, without prejudice to any other right and remedies available to PA under any law, contract or other instrument, be voidable at the option of PA.
any law, contract of other instrument, be voldable at the option of 1 A.
Motwithstanding any rights and remedies exercised by PA in this regard,
[Name of Supplier/Contractor/Consultant] agrees to indemnify PA for any loss or damage incurred by it on
account of its corrupt business practices and further pay compensation to PA in an amount
equivalent to ten time the sum of any commission, gratification, bribe, finder's fee or
kickback given by [Name of Supplier/Contractor/Consultant] as aforesaid for the purpose of obtaining or
inducing the procurement of any contract, right, interest, privilege or other obligation or
benefit, in whatsoever form, from PA.
(au)
[Procuring Agency] [Supplier /Contractor/Consultant]